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August 14, 2008

Neil S. Battfeld, Ph.D., Reg. No. 39,901

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INFORMATION DISCLOSURE STATEMENT

Applicant

Ayaki et al.

App. No

10/582,190

Filed

March 21, 2007

For

TAPE WITH BUILT-IN IC CHIP AND

SHEET WITH BUILT-IN IC CHIP

Examiner

Brent T. O Hern

Art Unit

1794

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing 11 (eleven) references to be considered by the Examiner. Also enclosed are 4 (four) foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits, and presumably no fee is required. If a first Office Action on the merits was mailed before the mailing date of this Statement, the Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dotad.

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